Formalizing a Framework for Dynamic Slicing of Program Dependence Graphs in Isabelle/HOL

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1 sum := 0;
2 prod := 1;
3 while (i>0) {
4 sum := sum+i;
5 prod := prod*i;
6 i := i-1;
}
7 out:=sum;
```

#### Task:

For a given program trace, find all statements that can have *influenced* the last statement *s*.

- $\Rightarrow$  Values used/computed by s
- $\Rightarrow$  Execution of *s*

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CFG:



Truc

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values  $\Rightarrow$  data dependences execution  $\Rightarrow$  control dependences



- $1 \, \text{sum} := 0;$
- 2 prod := 1;
- while (i>0) { 3
- sum := sum+i; 4
- prod := prod\*i;
- i := i-1;

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Slice:

CFG: 
$$\begin{array}{c} \hline True \\ False \\ \hline Slice: \\ ed \\ ed \\ \hline brod:=brod*i \\ \hline brod*$$

i> 0

Influence is

- defined in terms of semantics,
- approximated by data and control dependence

### Correctness property for slicing:

No other statements affect the values computed at the slicing criterion (or its execution).

Applications of slicing exploit this property:

- Debugging
- Compiler technology
- Software security

۰.

## Aim

Previous correctness proofs suffer from

- being only for while language
- depending on specific program languages
- not being machine-checked
- having to be redone for every new programming language

but slicing algorithms are independent of the programming language

#### Goal:

Show that no node outside the slice has any semantic influence

- independent of specific programming languages
- as modular as possible
- in Isabelle/HOL

# Module: Control Flow Graph

The *control flow graph (CFG)* is the abstract program representation:

Nodes: Set *valid-node* and special nodes *Entry*, *Exit* 

Edges: Edge  $a \in valid\text{-edge}$  between src a and trg a.



Semantics: kind labels edges with state predicates or transfer functions

Instantiate for specific programming languages to get:

Paths:  $n \rightarrow as \rightarrow n'$  runs from n to n' via edges as

Execution: transfer (kind a) s executes a's transfer functions on state s, pred (kind a) s checks if s satisfies a's predicate; transfers and preds fold these over lists

Control *n* controls *n'* via as dependence: Standard (static) control dependence and  $(n - as \rightarrow * n')$  Model effect of transfer functions and evaluation of predicates: *Def n* set of locations that *n*'s edges can affect *Use n* set of locations that *n*'s edges can depend on *sval* retrieves the location's value in a state
Assume: They correctly model the semantics of edge labels

Example:

States Mappings from {i, prod, sum} to Z
sval Function application
Use 5 {i, prod}

Def 5 {prod}



## Well-formedness constraints for modelling effects

Affected locations are in Def

 $\frac{a \in valid\text{-edge} \quad V \notin Def(src a)}{sval (transfer (kind a) s) V = sval s V}$ 

Opdates use only declared locations

 $\frac{a \in valid\text{-edge}}{sval (transfer (kind a) s) V = sval s' V} \quad V \in Def(src a)$ 

Predicates depend only on used locations  $\frac{a \in valid\text{-edge}}{pred (kind a) s = pred (kind a) s'}$ 

#### Dynamic data dependence

n influences V in n' via as:

- $V \in Def n$  n defines location V
- $V \in Use n' n'$  uses V, and
- $n \rightarrow as \rightarrow n'$  Nodes inside as do not define V inbetween.



## Program dependence graph

Combine control and data dependences in the program dependence graph (PDG) to get dependence paths  $n - as \rightarrow_{d^*} n'$ 

### Dynamic PDG / slicing:

- *Remember* CFG paths in dependence edges
- Match program trace with path information

## Static PDG / slicing:

- Abstract from CFG paths in dependence edges
- $\Rightarrow$  Reachability analysis on the PDG
  - Overapproximates dynamic slices



- Take an executable program trace n −as→\* n' with initial state s and final state s' = transfers (kinds as) s.
- Ompute dynamic slice bs for as
- For all nodes not in *bs*, replace outgoing transfer functions with no-ops and predicates with *True*, to get *as'*.
- Then *preds* (*kinds as'*) *s*, i.e. *as'* is executable,
  - and in the resulting state s'' = transfers (kinds as') s: sval s' V = sval s'' V for all  $V \in Use n'$



#### Proof: Induction on as

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# $\begin{array}{c|c} \text{Entry} & \textbf{True} & 1 & \textbf{sum:=0} & 2 \\ & & prod:=1 \\ \hline & & & \textbf{sum:=sum+i} & \textbf{4} & \textbf{i>0} \\ & & & \textbf{3} \\ & & & \textbf{prod:=prod*i} \\ \hline & & & \textbf{6} & \textbf{i:=i-1} & \textbf{3'} & \textbf{i\leq0} & \textbf{7} \end{array}$



### Proof: Induction on as fails!

## Dependent live variables

**Live variable analysis (LVA):** What variables (locations) are used in the trace before being defined again?

**Dependent live variables (DLV):** Consider *Def/Use* sets of non-slice nodes to be empty for LVA

#### Induction invariant:

- s<sub>1</sub> and s<sub>2</sub> agree on the (current) set of DLV
- Execute the original and sliced trace one step each for s<sub>1</sub> and s<sub>2</sub>
- Then, the resulting states agree on the (new) DLV set again



For the trace [3, 4, 5, 6, 3, 7]: Live variables: *i*, prod, sum Dependent live variables: *i*, sum

## Strengthened correctness statement for slicing

- Take an executable program trace  $n as \rightarrow * n'$  with initial state  $s_1$  and final state  $s_1' = transfers$  (kinds as)  $s_1$ .
- 2 Let  $s_2$  agree with  $s_1$  on DLV of as.
- Sompute dynamic slice bs for as
- For all nodes not in *bs*, replace outgoing transfer functions with no-ops and predicates with *True*, to get *as'*.
- Then preds (kinds as')  $s_2$ , i.e. as' is executable,

and in the resulting state 
$$s_2' = transfers$$
 (kinds as')  $s_2$ :  
sval  $s_1' V = sval s_2' V$  for all  $V \in Use n'$ 

preds es s  $\forall V \in Use n'. sval (transfers es s) V = sval (transfers es' s') V$ 

# Summary

Framework for dynamic slicing based on CFGs/PDGs

- Generic correctness proof
- Instantiable for specific programming languages
- Highly modularized

#### Context: Quis custodiet project

- Generic framework for slicing in Isabelle/HOL
  - Different control dependences
  - Static intraprocedural slicing
  - Static interprocedural slicing
  - Instantiated for a While language
  - Realistic languages (Jinja, CoreC++) future work
- Verifying software security analyses / algorithms

future work